

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	23806	(integrat\$3 black color monochrom\$4) near3 (circuit\$3 chip format\$3) same (interconnect\$3 connect\$3) with (bus\$3 interface "I/O")	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:19
2	BRS	L2	10	(black monochrom\$4 color) near3 (print\$3 record\$3) with (interchang\$5 interconnect\$) same (integrat\$3 driv\$3) with (chip circuit\$3 bus storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:46
3	BRS	L3	3871	347/15,40-43.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:29
4	BRS	L4	45	3 and (1 2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:30
5	BRS	L5	0	(black monochrom\$4) near3 (print\$3 record\$3) with (interchang\$5 interconnect\$) same color adj4 (integrat\$3 driv\$3) near4 (chip circuit\$3 bus storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:54
6	BRS	L6	1	(black monochrom\$4) near3 (print\$3 record\$3) with (interchang\$5 interconnect\$) and color adj4 (integrat\$3 driv\$3) near4 (chip circuit\$3 bus storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:58
7	BRS	L7	44	1 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:57
8	BRS	L8	29	(black monochrom\$4) near3 (print\$3 record\$3) same (interchang\$5 interconnect\$) and color adj4 (integrat\$3 driv\$3 chip circuit\$3 bus storage)	US-PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:59

9	BRS	L9	3	3 and 8	US- PGPUB; USPAT; EPO; JPO; DERWENT	2005/12/08 11:59
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